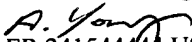


**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant : **HEILBRONNER et al.**  
Serial No. : Unassigned  
Filed : Herewith  
For : **LOW-INDUCTANCE CIRCUIT ARRANGEMENT FOR  
POWER SEMICONDUCTOR MODULES**  
Examiner : Unassigned  
Art Unit : Unassigned  
Docket No. : Y3-6

**CERTIFICATE OF EXPRESS MAIL**

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail, in an envelope addressed to: Hon. Assistant Commissioner for Patents, Arlington, VA 22202 on:

Date : August 18, 2003  
By : Andrew F. Young, Esq.  
Signature :   
Customer Receipt No.: ER 24154444 US

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**Application Information and Assignment Information**

Utility Patent App. with 36 pages, 5 drawings on 5 sheets  
29 Total claims, 4 independent.

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**Domestic Priority Claim Information**

No domestic Priority Claim is made herein

**Foreign Priority Claim Information**

This application claims foreign priority under the applicable code sections (35 U.S.C. §119 or § 365) from DE 102 37 561.5, filed August 16, 2003. A specific request is made herein to record this priority claim in the appropriate records at the U.S. Patent and Trademark Office and to amend the application as needed.

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Date:

*8/18/03*